

Phone number
City
Email

Your Name

XX's Blog
GitHub: github
LinkedIn: linkedin

EDUCATION

Master of XX **Mon Year - Mon Year (expected)**
XX university

Main courses: Digital Signal Processing, Digital Image Analysis, Combinational Mathematics

Bachelor of XX **Mon Year - Mon Year**
XX university

Main courses: Signal and Systems, Analog and Digital Circuits, Data Structures, C Programming

WORK EXPERIENCE

Hardware Engineer **Mon Year - Mon Year**
XX company *Location*

- Designed hardware schematics and PCB diagrams that received and processed input signals by using AltiumDesigner.
- Developed FPGA module that measured frequencies of multiple signal by using verilog programming and ModelSim simulating skills, and achieved 99.9% accuracy.

Software Test Engineer **Mon Year - Mon Year**
XX company *Location*

- Achieved 1000+ test cases for monitor software using functional and automated testing skills.
- Optimized test cases that improved test coverage and guaranteed reliability of software version updates.

Internship **Mon Year - Mon Year**
XX company *Location*

- Learned the detailed principles and operations of CT and other medical instruments in practical, deeply understood the application.

PROJECT

Research on XX **Mon Year**
Bachelor's Thesis

- Compared the reservoir computing grid model before and after the addition of positive and negative emotion regulation mechanisms by using Matlab programming.
- Improved the accuracy of the algorithm in predicting the spatial and temporal evolutionary features of the dynamical system by adjusting the activation function and specific parameters of the neural grid.

SKILLS

Programming Python, Java, C, C++, Git, L^AT_EX, Matlab, Markdown

Hardware Design Verilog, Quartus2, ModelSim, AltiumDesigner

Communication Chinese (native), English (business), Japanese (beginner)

Other Github, Microsoft Office